

Electronic Acknowledgement Receipt

EFS ID:	5360276
Application Number:	10591193
International Application Number:	
Confirmation Number:	5900
Title of Invention:	Dft Technique for Stressing Self-Timed Semiconductor Memories to Detect Delay Faults
First Named Inventor/Applicant Name:	Mohamed Azimane
Customer Number:	65913
Filer:	Mark Alan Wilson
Filer Authorized By:	
Attorney Docket Number:	US04 0145 US3
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1		US040145US3_OAR_021909.pdf	144083 bb997bc1bb0fb3812fb96062803c2d9e747b5e9	yes	17

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment/Req. Reconsideration-After Non-Final Reject	1	1
	Claims	2	6
	Applicant Arguments/Remarks Made in an Amendment	7	17

Warnings:

Information:

Total Files Size (in bytes):	144083
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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